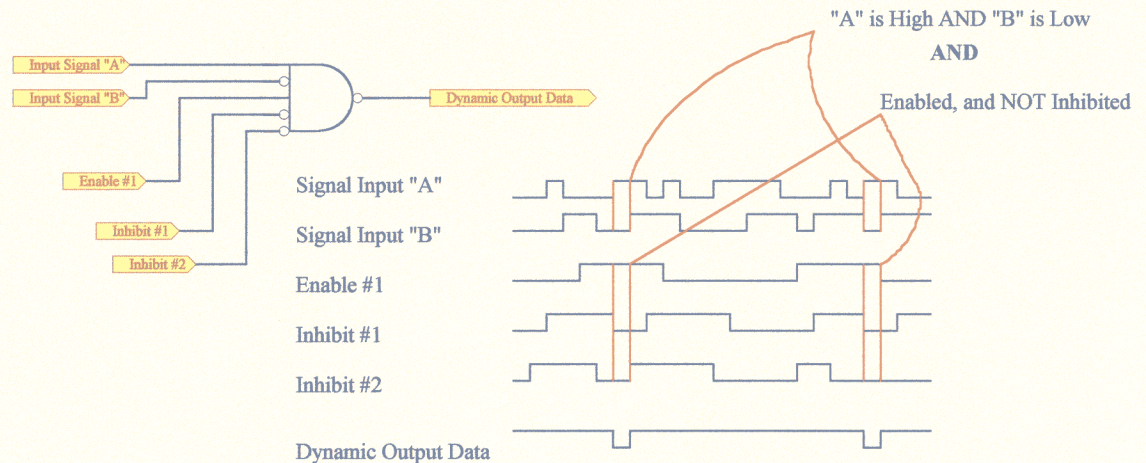


Coincidence Gating of 2 Signal Inputs, when Enabled and NOT Inhibited

Note that the Gate is ONLY Active when Enabled by "Enable #1",
and NOT Inhibited by "Inhibit #1", or "Inhibit #2".

When the Gate is Active, the Data Output will be Driven Low

ONLY when Data Input "A" is High, and Data Input "B" is Low.



Title Coincidence Gating, with Enable/Inhibit Controls		
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File: C:\CLIENT 3\COINCID2.SCH	Drawn By: SfE-DCS, ddf	