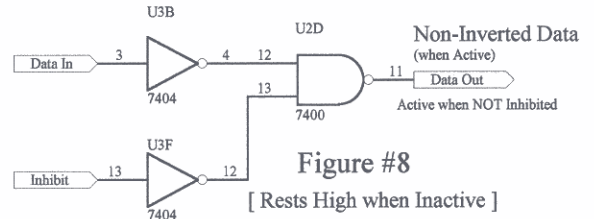
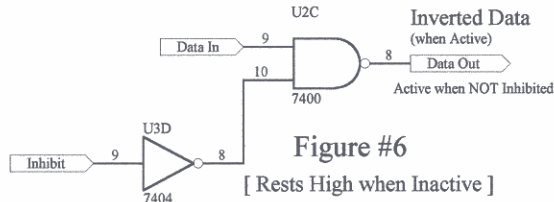
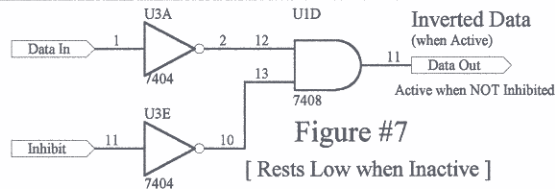
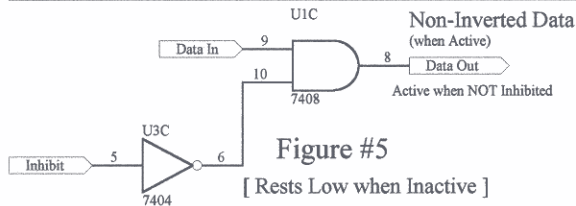


## Enables with a High (allowing the Gate to go Active and Pass Data)



## Inhibits with a High (Causing the Gate to go Inactive and Block Data)

Title		
<b>Dynamic-Signal Gate-Analysis</b>		
Size	Number	Revision
A	<b>Solutions For Education</b>	1
Date:	30-Jun-2001	Sheet of 2
File:	C:\CLIENT_3\3DYNAMIC3.SCH	Drawn By: SFE-DCS, ddf