Figure #1
Non-Inverted Data (when Active)
Active when Enabled
[Rests Low when Inactive]

Figure #3
Inverted Data (when Active)
Active when Enabled
[Rests Low when Inactive]

Figure #2
Inverted Data (when Active)
Active when Enabled
[Rests High when Inactive]

Figure #4
Non-Inverted Data (when Active)
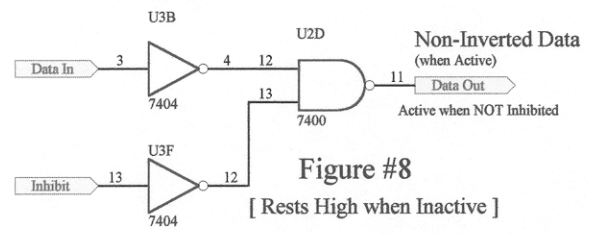Active when Enabled
[Rests High when Inactive]

Enables with a High
(allowing the Gate to go Active and Pass Data)

Figure #5
Non-Inverted Data (when Active)
Active when NOT Inhibited
[Rests Low when Inactive]

Figure #7
Inverted Data (when Active)
Active when NOT Inhibited
[Rests Low when Inactive]

Figure #6
Inverted Data (when Active)
Active when NOT Inhibited
[Rests High when Inactive]

Figure #8
Non-Inverted Data (when Active)
Active when NOT Inhibited
[Rests High when Inactive]
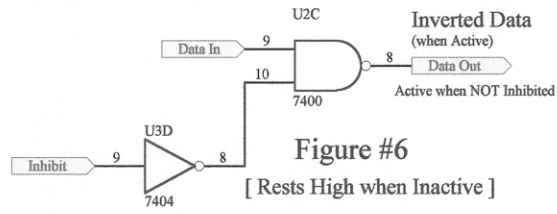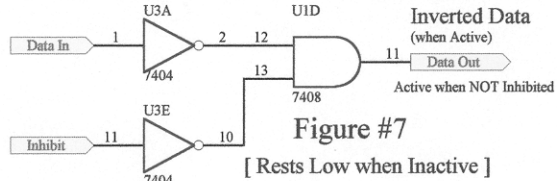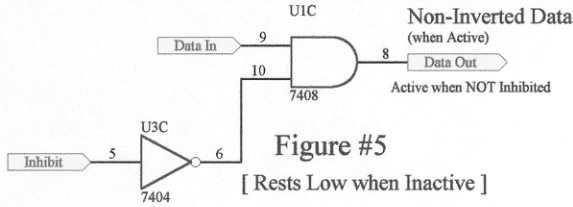
Inhibits with a High
(Causing the Gate to go Inactive and Block Data)

Title	Dynamic-Signal Gate-Analysis	
Size	Number	Revision
A	Solutions For Education	1
Date: 30-Jun-2001		Sheet of 2
File: C:\CLIENT_3\DYNAMIC3.SCH		Drawn By: SfE-DCS, ddf